

(Foreign associate use only) **DECLARATION AND POWER OF ATTORNEY**

Attorney's Docket No. 44471-265522 (13700)

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated be	elow next to my name. I believe I am the original, first and sole
inventor (if only one name is listed below) or an original, first an	id joint inventor (if plural names are listed below) of the subject
matter which is claimed and for which a patent is sought on the in	vention entitled: LAIERAL TRANSISION HAVING GRADED BASE REGION,
SEMICONDUSIOR INTEGRATED CIRCUIT AND , the specificati	on of which
was filed on as U.S. Application or PCT Inte	HEREOF rnational Application No and was amended (if applicable) on
·	**
I hereby state that I have reviewed and understand the content	
amended by any amendment referred to above. I do not know an	
in the United States of America before my or our invention the	
country before my or our invention thereof or more than one ye	
invention was not in public use or on sale in the United States of	
I understand that I have a duty of candor and good faith toward	
disclose information which is material to patentability as defined i	in Title 37, Code of Federal Regulations, §1.56.
71 1 1 1 C 1 1 C 1 C 1 C 1 C 1 C 1 C 1 C	
I hereby claim foreign priority benefits under Title 35, United Sta	
patent or inventor's certificate, or §365(a) of any PCT internation	
the United States of America, listed below and have also ide	
certificate disclosing subject matter in common with the above-ic	dentified specification and having a filing date before that of the
application on which priority is claimed:	D. I. O. I. IV. I. AFVIGGALIA
App. No. Date of Filing P2001-128187 April 25, 200	Priority Claimed Under 35 USC §119
## P2001-120107 API 11 25, 200	1 YesXNo
I hereby claim the benefit under Title 35, United States Code, §	120 of any prior United States application(s) or \$365(c) of any
PGT international application designating the United States of A	
claim of the present application is not disclosed in the prior Unite	
by the first paragraph of Title 35, United States Code §112, I ac	
patentability as defined in Title 37, Code of Federal Regulation	
prior application and the national or PCT international filing date	
Application No. Filing Date	Status: patented, pending, abandoned
, t = 1	
Light further declare that all statements made herein of my own known	
belief are believed to be true; and further that these statement we	
like so made are punishable by fine or imprisonment, or both, un	
such willful false statements may jeopardize the validity of the ap	plication or any patents issuing thereon.
The day of the division of the	0.17
I hereby authorize the U.S. attorneys named herein to accept and	
to be taken in the Patent and Trademark Office regarding this app	
and the undersigned. In the event of a change in the persons from	whom instructions may be taken, the U.S. attorney named herein
will be notified by the undersigned.	
DOWNER OF ASSESSMENT FOR CITY OF THE CITY	
POWER OF ATTORNEY: The following attorneys are hereby app	
the Patent and Trademark Office connected therewith: Anthony E	
29,105; Stephen M. Schaetzel -31,418; Larry A. Roberts - 31,871	
L. Greene-32,467; Mary Anthony Merchant - 39,771; Brenda O	
Green - 41,801; John K. McDonald - 42,860; Suzanne Seavello S	
Chan - 44,070; John M. Briski - 44,562; S. Craig Hemenway -	
Cheryl L. Huseman - 45,392; Shelby B. Grier - 45,785; Vaibh	av P. Kadaba - 45,865; Donald R. Andersen - 28,280; Joseph
Bennett-Paris - P47,226	
John S. Pratt	
Send correspondence to: KILPATRICK STOCKTON, LLP	Direct telephone calls (404) 815-6500 to:
Suite 2800, 1100 Peachtree Street	Roger T. Frost
Atlanta, GA 30309-4530	
Full name of sole or first inventor: Makoto YAMAMOT	0
Citizenship: Japan	
Residence: Saitama-ken, Japan	
Post Office Address: c/o Sanken Electric Co., Ltd.	, 3-6-3, Kitano, Niiza-shi, Saitama-ken, Japan
Inventor's signature Makoto Varma on 10	Date: October 22,2001

 $[\]fbox{X}$ Additional inventors are being named on separately numbered sheets attached hereto.

Attorney Docket No.: 44471-265522 (13700)

Title: Lateral Transistor Having Graded Base Region, Semiconductor Integrated Page 2 Circuit And Fabrication Method Thereof

Page 2

Full name of second joint inventor, if any: Akio IWABUCHI
Citizenship: Japan
Residence: Saitama-ken, Japan
Post Office Addressc/o Sanken Electric Co., Ltd., 3-6-3, Kitano, Niiza-shi, Saitama-ken, Japan
Inventor's signature Ali Qualuli Date: October 22, 2001
Full name of third joint inventor, if any:
Citizenship:
Residence:
Post Office Address:
Inventor's signature Date:

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